

Semester-wise Pattern for Students Admitted to B.E. Electrical & Electronics Programme

Year	First Semester			U	Second Semester			U
I	BIO	F110	Biology Laboratory	1	MATH	F112	Mathematics II	3
	BIO	F111	General Biology	3	ME	F110	Workshop Practice	2
	CHEM	F110	Chemistry Laboratory	1	CS	F111	Computer Programming	4
	CHEM	F111	General Chemistry	3	EEE	F111	Electrical Sciences	3
	MATH	F111	Mathematics I	3	BITS	F112	Technical Report Writing	2
	PHY	F110	Physics Laboratory	1	MATH	F113	Probability and Statistics	3
	PHY	F111	Mechanics, Oscillations and Waves	3	BITS	F111	Thermodynamics	3
	BITS	F110	Engineering Graphics	2				
			17				20	
II	MATH	F211	Mathematics III	3	ECON	F211	Principles of Economics	3
			Humanities Electives	3(min)			or	or
	EEE	F211	Electrical Machines	4	MGTS	F211	Principles of Management	3
	EEE	F212	Electromagnetic Theory	3			Humanities Electives	3(min)
	EEE	F215	Digital Design	4	EEE	F241	Microprocessors & Interfacing	4
	EEE	F214	Electronic Devices	3	EEE	F242	Control Systems	3
					EEE	F243	Signals & Systems	3
			20(min)	EEE	F244	Microelectronic Circuits	3	
							19(min)	
Summer BITS F221 Practice School – I (for PS Option Only)								
III			Open/Humanities Electives	4to6			Open/Humanities Electives	3to6
	EEE	F311	Communication Systems	4	EEE	F341	Analog Electronics	4
	MATH	F212	Optimization	3	EEE	F342	Power Electronics	4
			or		EEE	F312	Power Systems	3
	ME	F344	Engineering Optimization	2			Discipline Electives	4(min)
	EEE	F313	Analog & Digital VLSI Design	3				
			Discipline Electives	5(min)				
			18/21				18/21	
IV			Open Electives	5to11	BITS	F412	Practice School-II	20
			Discipline Electives	3(min)			or	or
					BITS	F421T	Thesis	16
							or	
						Thesis (9) and Electives (6 to 9)	15to18	
			8/14				15/20	

Discipline Core - 47 or 48 Units (14 Courses)

Discipline Electives - 12 Units (4 Courses)

Note: This is operative pattern for the students who are admitted from August 2011 onwards as approved by the Senate-appointed committee, subject to change if the situation warrants.