Ref: DST/NSM/R&D_Exascale/2021/13   Date: 25/03/2021

Research Associate (RA) Recruitment in National Super Computing Mission, DST Sponsored Project

Applications are invited from eligible candidates to work as a Research Associate (RA) in a Department of Science and Technology (DST) sponsored project at Electrical and Electronics Engineering Department, BITS-Pilani, (Pilani campus), Rajasthan. The details are as follows:

Project Work	Design & Development of hybrid Memories for High Performance Computing
Project Duration	2 Years
Principal Investigator (PI)	Dr. Nitin Chaturvedi
Fellowship Amount	Rs. 54,000/-
Essential Qualifications:	Ph.D or M.Tech/M.E having 3 years of research/design & development experience with at least 60% marks in any domain VLSI, Microelectronics, Embedded Systems with at least one SCI journal.
Desirable Qualification:	Preference will be given to Ph.D candidates having sound knowledge of Digital/Analog VLSI design, ASIC/Full Custom Design flow. Programming Skills in any of the following languages-Spice, VHDL/Verilog/python.

Application Procedure:

- Candidates with the above mentioned qualification should email their CV to the PI (nitin80@pilani.bits-pilani.ac.in) by 05th April 2021 with the subject of the mail as 'Application for RA position –DST project'.
- Shortlisted candidates will be informed through email and called for an interview. Interview will be conducted either online or in-person at the Electrical and Electronics Engineering Department, BITS-Pilani, (Pilani Campus), Rajasthan.

Notes:

1.For any queries regarding the position, please feel free to email the PI.
2.The position mentioned is temporary and for the period of duration of the project. If the performance of the candidate is found unsatisfactory, the position can be terminated with a 1-month notice.
3.It is mandatory for the selected candidate to join department and work in BITS Pilani, Pilani campus.
4.No TA/DA will be paid for attending the interview

Dr. Nitin Chaturvedi (PI)

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