



Birla Institute of Technology & Science, Pilani
Hyderabad Campus

**CALL FOR JUNIOR RESEARCH FELLOW FOR AN INDUSTRY SPONSORED
PROJECT (REDPINE SIGNALS INC)**

Date: 16th May, 2020

Applications are invited from suitable candidates for **Junior Research Fellow (JRF)** position to work on the **Industry Project** sponsored by **Redpine Signals Inc.** under the supervision of Dr. Parikshit Sahatiya, Assistant Professor, Department of Electrical and Electronics Engineering, BITS-Pilani, Hyderabad campus. The project involves the fabrication and characterization of **Flexible Memristors** and their application in AC signals analysis.

Duration of the project: 2 years

Project position: Junior Research Fellow

Number: 1

Fellowship amount: 31,000 pm consolidated

Eligibility requirements: M. Tech (VLSI, Microelectronics, Nanotechnology, ECE, EEE and allied areas). Candidates who are about to graduate by July 2020 are also eligible.

Interested candidates with the above mentioned qualification can send their applications by email giving detailed information about their educational qualifications, research experience and published research papers if any, by 15th June, 2020, to the Principal Investigator at **parikshit@hyderabad.bits-pilani.ac.in**. Candidates with prior experience in **Synthesis and Characterization of Nanomaterials** and strong background of **Device Physics** with **GATE qualification** is desirable.

Please note that only qualified and suitable candidates will be notified for Interview via email. Interview would be through Skype/Google Meet.

Selected candidate may be permitted to register for Ph.D. program of BITS, Pilani, subject to the fulfilment of the requirements.

Best Regards,
Dr. Parikshit Sahatiya
EEE Department, BITS Pilani Hyderabad Campus



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